

6.5.0 OS Feature Set:

- ARM Cortex A9 SMP support
- POSIX PSE52 Certification
- [Improved ppc interrupt to thread latency](#)
- [Persistent Publish Subscribe \(pps\)](#)
- [proc free list optimization](#)
- [cross-endian support for some procfs devctls](#)
- [Memory Mapping Optimization \(Dynamic Skip List\)](#)
- [heap free list optimization](#)
- [Support for e500mc](#)
- [Optimized Message Passing on e500mc](#)

What can I do?#

- Interested in contributing to feature development for 6.5.0? Contact us via the [OS Dev Forum](#) .
- Like to participate in the code reviews for 6.5.0? Visit the [OS Rev Forum](#) and offer your critique.
- 6.5.0 binaries are not yet available but the [source code](#) is. If you have any comments or spot a bug, bring it to the attention of the team through the [forums](#).