

# Xilinx Zynq7000 ZC702 EVM Board Support Package#

This BSP supports the Xilinx Zynq7000 All Programmable [SoC](#), on the Xilinx ZC702 Evaluation Kit.

## Releases#

### Updated! QNX Neutrino 6.6.0 BSP#

| Date       | Version | Prerequisites | User Guide           | License(s) | Support      | Provider | BSP                  |
|------------|---------|---------------|----------------------|------------|--------------|----------|----------------------|
| 2018-01-11 | 6.6.0   | QNX SDP 6.6.0 | <a href="#">here</a> | Apache II  | Experimental | QNX      | <a href="#">here</a> |

## Features#

| Feature        | Planned Availability | Format | Notes |
|----------------|----------------------|--------|-------|
| IPL            | Completed            | SRC    |       |
| Startup        | Completed            | SRC    |       |
| Serial         | Completed            | SRC    |       |
| I2C            | Completed            | SRC    |       |
| SPI            | Completed            | SRC    |       |
| CAN            | Completed            | SRC    |       |
| QSPI NOR flash | Completed            | SRC    |       |
| USB EHCI Host  | Completed            | BIN    |       |
| Ethernet       | Completed            | SRC    |       |
| RTC            | Completed            | SRC    |       |
| SD/MMC         | Completed            | SRC    |       |
| FPGA utility   | Completed            | SRC    |       |
| XADC utility   | Completed            | SRC    |       |
| OCM utility    | Completed            | SRC    |       |
| GPIO library   | Completed            | SRC    |       |
| DMA library    | Completed            | SRC    |       |

## QNX Neutrino SDP 6.6 Change History#

### January 11, 2018#

- SDMMC: updated driver to new devb-sdmmc framework
- Networking: added getmac-i2c utility to read Ethernet MAC from I2C EEPROM. Removed MAC address spoofing when EEPROM read fails.
- QSPI-Flash: Errata AR# 47575 fix. Support for 4-byte addressing
- UART: support for multiple interfaces
- I2C: fixed i2c send issue, fixed clock issue
- SPI: fixed Chip Select behavior
- GPIO Lib: various bug fixes

**August 20, 2014#**

- initial post

### **QNX Neutrino 6.5.0 Service Pack 1 BSP#**

| <b>Date</b> | <b>Version</b> | <b>Prerequisites</b>    | <b>User Guide</b>    | <b>License(s)</b> | <b>Support</b> | <b>Provider</b> | <b>BSP</b>           |
|-------------|----------------|-------------------------|----------------------|-------------------|----------------|-----------------|----------------------|
| 2014-08-18  | 6.5.0 SP1      | QNX Momentics 6.5.0 SP1 | <a href="#">here</a> | Apache II         | Experimental   | QNX / Adeneo    | <a href="#">here</a> |

### **Features#**

| <b>Feature</b> | <b>Planned Availability</b> | <b>Format</b> | <b>Notes</b> |
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| CAN            | Completed                   | SRC           |              |
| QSPI NOR flash | Completed                   | SRC           |              |
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| Ethernet       | Completed                   | SRC           |              |
| RTC            | Completed                   | SRC           |              |
| SD/MMC         | Completed                   | SRC           |              |
| FPGA utility   | Completed                   | SRC           |              |
| XADC utility   | Completed                   | SRC           |              |
| OCM utility    | Completed                   | SRC           |              |
| GPIO library   | Completed                   | SRC           |              |
| DMA library    | Completed                   | SRC           |              |

### **QNX Neutrino 6.5.0 SP1 Change History#**

**August 18, 2014#**

- updated GIC interrupt callout included in BSP, to reduce potential interrupt latency

**January 24, 2014#**

- include prebuilt boot.bif and fsbl.elf in BSP
- documentation updates to clarify process for building IPL binary with different versions of Xilinx tools
- minor fix for an incorrect #define in the startup code

**June 14, 2013#**

- add QSPI NOR flash driver source and DMA Library source
- fix for [ClockPeriod\(\)](#) issue (use global timer)
- fix for [ClockCycles\(\)](#) issue

- update to enable FPGA access after FPGA is programmed

## **May 15, 2013#**

- update to address minor issues:
  - don't start watchdog timer by default in build file
  - include pre-built DMA library
  - remove duplicate wdtkick utility

## **May 14, 2013#**

## **Release 1.00#**

- Initial post

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*Search Keywords: xilinx, zynq, zynq-7000, zynq7000, zc702, zc706, AP [SoC](#), arm, cortex-a9, fpga*