

# Intel Crown Bay Board Support Package#

This BSP supports the Intel Crown Bay board (E6xx and EG20T aka [TunnelCreek/Topcliff](#)) in both Fastboot IPL and BIOS based configurations.

## Releases#

### QNX Neutrino 6.4.1#

Date	Version	Prerequisites	Installation Notes	Release Notes	License(s)	Support	Provider	Download
2011/11/02	trunk	QNX Momentics 6.4.1	<a href="#">6.4.x Generic Notes</a>	see doc folder in package archive	Apache II and QDL	Released	QNX	<a href="#">Project Downloads</a>

Crown Bay Fastboot IPL is [here](#)

## Features#

Feature	Planned Availability	Format	Notes
Fastboot IPL	Available	source	for non BIOS configurations. Requires SPI flash programmer
Startup	Available	source	startup-apic (for BIOS based IPL) and startup-crownbay (for Fastboot IPL)
UART	Available	source	devc-ser8250 (for use on the <a href="#">SuperIO</a> based UART), devc-serpci for EG20T IOH
MMC/SD	Available	source	
SATA AHCI	Available	source	
PCI server	Available	source	includes IOAPIC and MSI/MSI-X support
SMB	Available	source	system management bus resource manager
SPI	Available	source	master mode only
CAN	Available	source	loopback not supported
Ethernet	Available	binary	EG20T
USB	Available	binary	OHCI and EHCI

See `./docs/usr/help/product/bsp_crownbay/release_notes/release_notes.html` and `./src/hardware/startup/boards/**.build` files for details on building and configuring the appropriate bootable images and starting up applicable drivers

## **QNX Neutrino 6.4.1 Change History#**

### **Trunk#**

- Created from QNX Neutrino 6.4.1 BSP for Intel Crown Bay
- Built using the latest 6.4.1 toolchain

### **2011-11-02 update#**

- fixes for verification of CMC relocation during IPL