

Freescale MPC85x0 ADS Board Support Package#

This BSP supports the Freescale MPC85x0 ADS board.

Releases#

TRUNK#

| Date | Version | Prerequisites | Installation Notes | Release Notes | License(s) | Support | Provider | Download | Source Tag |
|--------------|---------|-------------------|-----------------------------------|----------------------|------------|--------------|----------|--------------|--------------|
| Coming Soon! | trunk | QNX Momentics 6.4 | 6.4 Generic Notes | here | Apache II | Experimental | QNX | Coming Soon! | Coming Soon! |

QNX Neutrino 6.4#

| Date | Version | Prerequisites | Installation Notes | Release Notes | License(s) | Support | Provider | Download | Source Tag |
|--------------|---------|-------------------|-----------------------------------|----------------------|------------|--------------|----------|--------------|--------------|
| Coming Soon! | 1.0.0 | QNX Momentics 6.4 | 6.4 Generic Notes | here | Apache II | Experimental | QNX | Coming Soon! | Coming Soon! |

QNX Neutrino 6.3.2#

| Date | Version | Prerequisites | Installation Notes | Release Notes | License(s) | Support | Provider | Download | Source Tag |
|------------|---------|----------------------------------|-------------------------------------|----------------------|------------|---------|----------|-------------------------------------|------------|
| 2005-01-21 | 1.0.2 | QNX Momentics 6.3.x SP1 or later | 6.3.2 Generic Notes | here | MEULA | QNX | QNX | QNX Download Center | none |

Features#

| Feature | Planned Availability | Format | Notes |
|----------|----------------------|--------|--|
| Ethernet | Completed | SRC | TSCE Ethernet controller |
| Flash | Completed | SRC | 16M Intel E28F640J3A-120 Flash |
| PCI | Completed | SRC | PCI controller |
| Serial | Completed | SRC | SCC UART devices on RS-232 port 1 and port 2 |
| Startup | Completed | SRC | . |

Some of these drivers are commented out in the default buildfile. To use the drivers in the target hardware, you'll need to uncomment them in your buildfile, rebuild the image, and load the image into the board.

TRUNK Change History#

Trunk#

- Created from QNX Neutrino 6.3.2 Centrality Atlas II BSP version 1.0.2
- Built using the latest 6.4 toolchain

6.4 Change History#

1.0.0#

- Created from QNX Neutrino 6.3.2 Centrality Atlas II BSP version 1.0.2
- Built using the latest 6.4 toolchain

6.3.2 Change History#

1.0.2#

- Initial release under MEULA
- Includes:
 - Serial
 - Startup
 - Ethernet
 - flash
 - pci