Altera Cyclone V SoC Development Kit Board Support Package#

This BSP supports the Altera Cyclone V SoC Development Kit

Releases#

New! QNX Neutrino 6.6.0 BSP#

Date	Version	Prerequisites	User Guide	License(s)	Support	Provider	BSP
2014-10-10	SDP 6.6	QNX SDP 6.6	**BSP User Guide**	Apache II	Experimental	QNX	**here**

pre-loader and u-boot#

Click **here** to download a binary file containing the pre-loader and u-boot.

Features#

Feature	Planned Availability	Format	Notes
Startup	Completed	SRC	
Serial	Completed	SRC	
I2C	Completed	SRC	
SPI	Completed	SRC	
CAN	Completed	SRC	
USB Host	Completed	BIN	
Ethernet	Completed	SRC	
RTC	Completed	SRC	
SD/MMC	Completed	SRC	
QSPI NOR flash	SRC		
NVRAM	SRC		

QNX Neutrino 6.6.0 Change History#

July 4, 2014#

- Initial post

July 27, 2014#

- added QSPI NOR Flash driver, updated docs

October 10, 2014#

Updated to remove pre-loader/u-boot image from BSP - now available from link above

QNX Neutrino 6.5.0 Service Pack 1 BSP#

Date	Version	Prerequisites	Release Notes	License(s)	Support	Provider	BSP
2014-04-17	1.50	QNX Momentics 6.5.0 SP1	here	Apache II	Experimental	Altera	here

Features#

Feature	Planned Availability	Format	Notes
Startup	Completed	SRC	
Serial	Completed	SRC	
I2C	Completed	SRC	
SPI	Completed	SRC	
CAN	Completed	SRC	
USB Host	Completed	BIN	
Ethernet	Completed	SRC	
RTC	Completed	SRC	
SD/MMC	Completed	SRC	
QSPI NOR flash	Completed	SRC	
LCD	Completed	SRC	
GPIO	Completed	SRC	
DMA	Completed	SRC	
NVRAM	Completed	SRC	

QNX Neutrino 6.5.0 Change History#

April 17, 2014#

- Release 1.50

February 10, 2014#

- Release 1.40

October 18, 2013#

Release 1.00#

- Initial post

Search Keywords: Altera,Cyclone V,CycloneV,Cyclone,ARM9,ARM Cortex A9